

#### ABSTRACT OF THE DISCLOSURE:

A silicon wafer with micro-reflector cavities for mounting LED dies is disclosed. The cavities are formed by means of anisotropic etching. The cavities may be lined with a metallic material with good electrical conducting properties. The cavity and LED die may be capped with an encapsulant material to further focus the LED light. The wafer is particularly well-suited for the placement of RGB LED dies in clusters for efficient LED illumination.